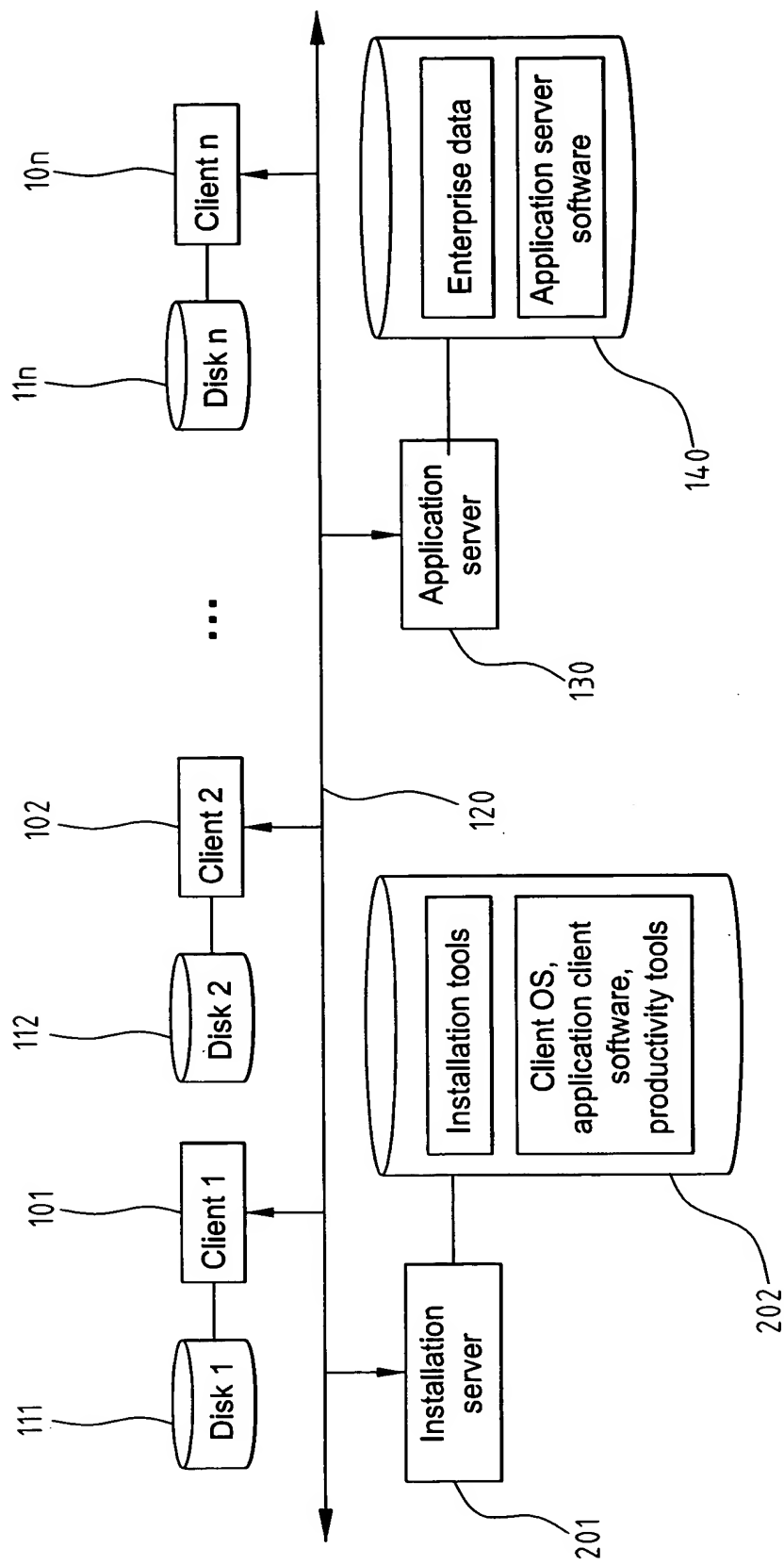


**FIG. 1 (PRIOR ART)**



**FIG. 2 (PRIOR ART)**

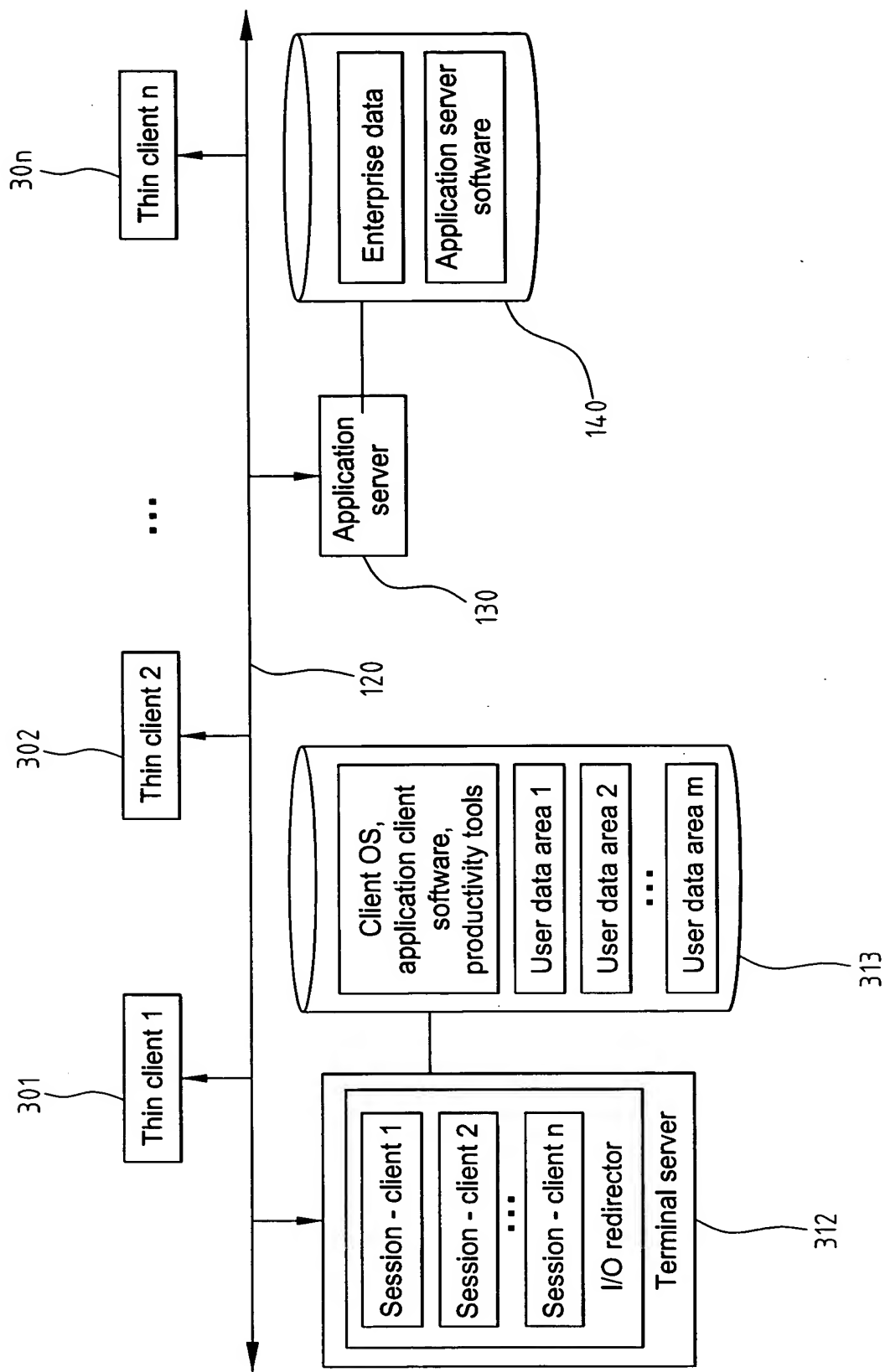
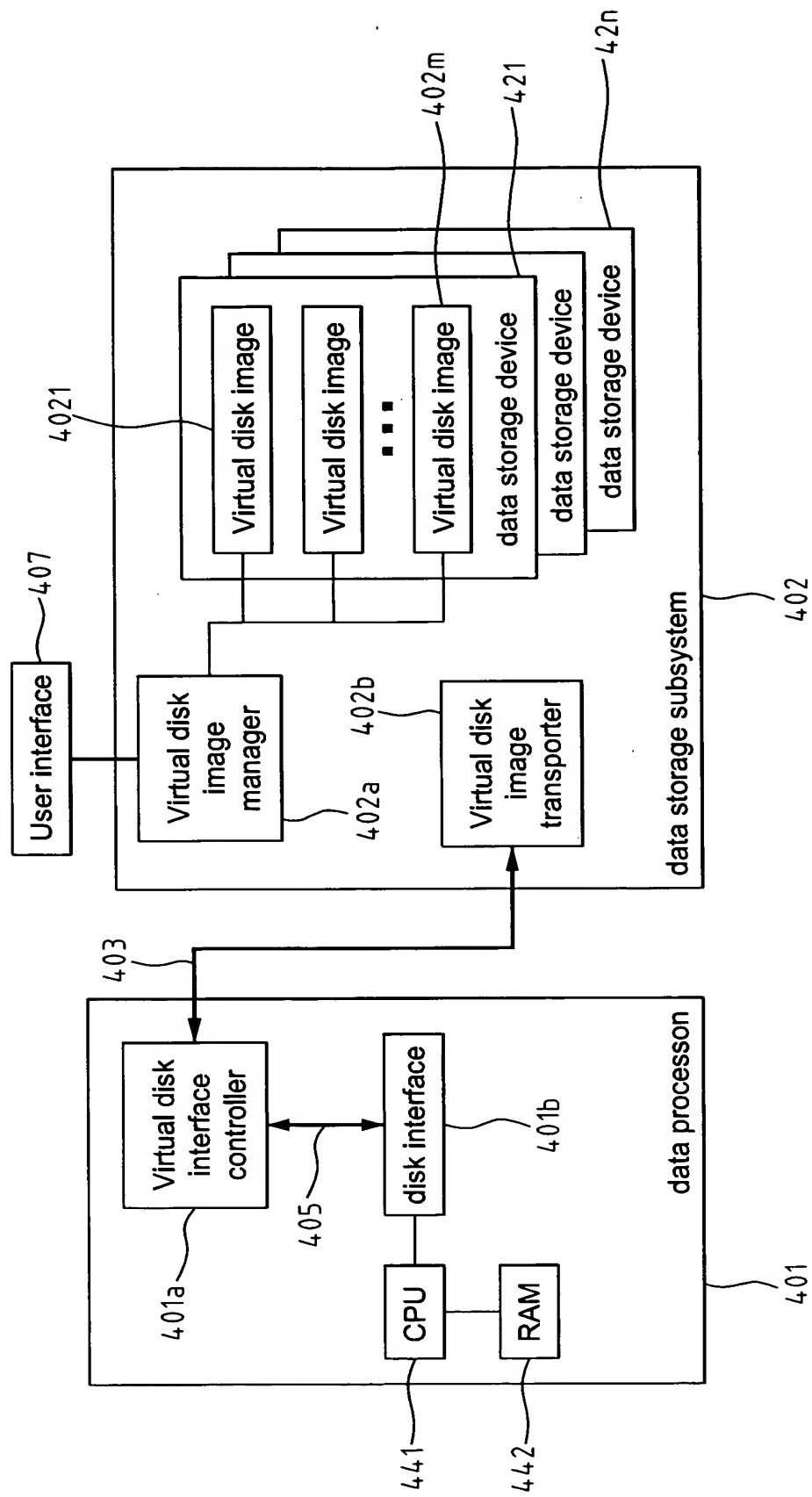


FIG. 3 (PRIOR ART)



**FIG. 4**

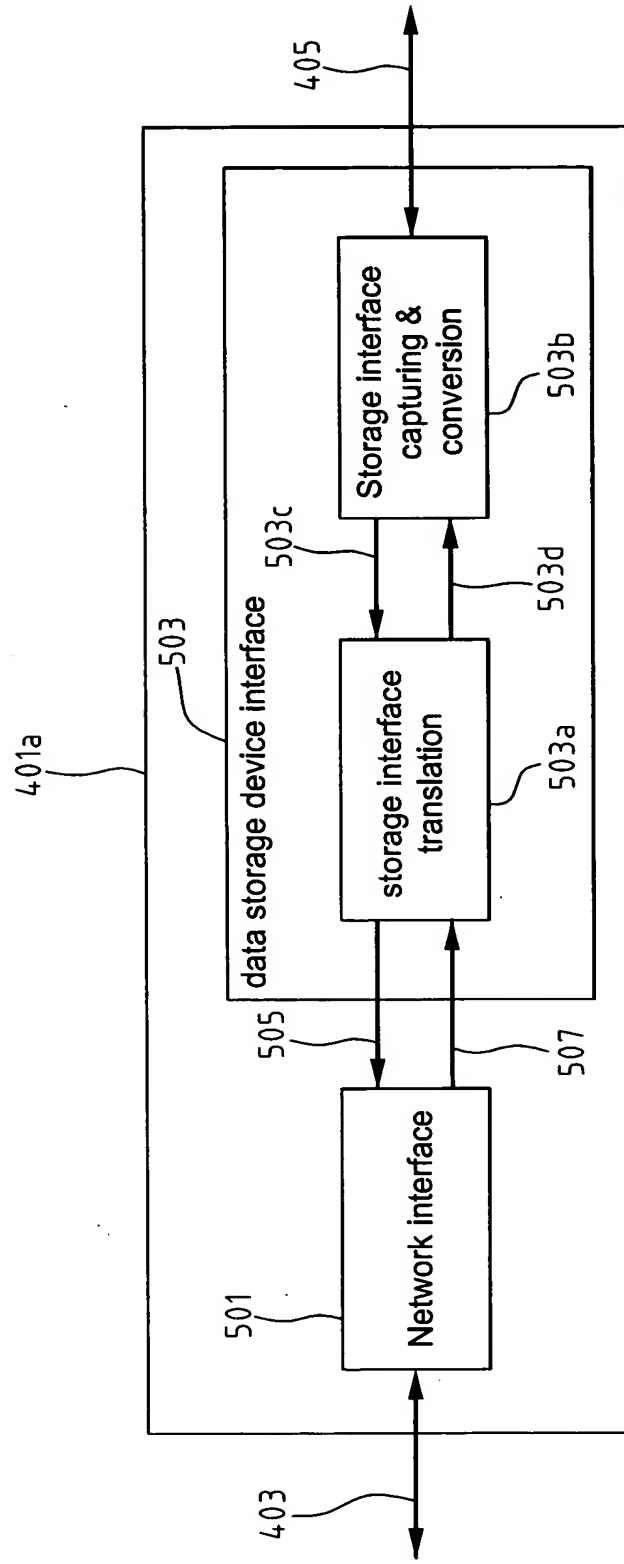
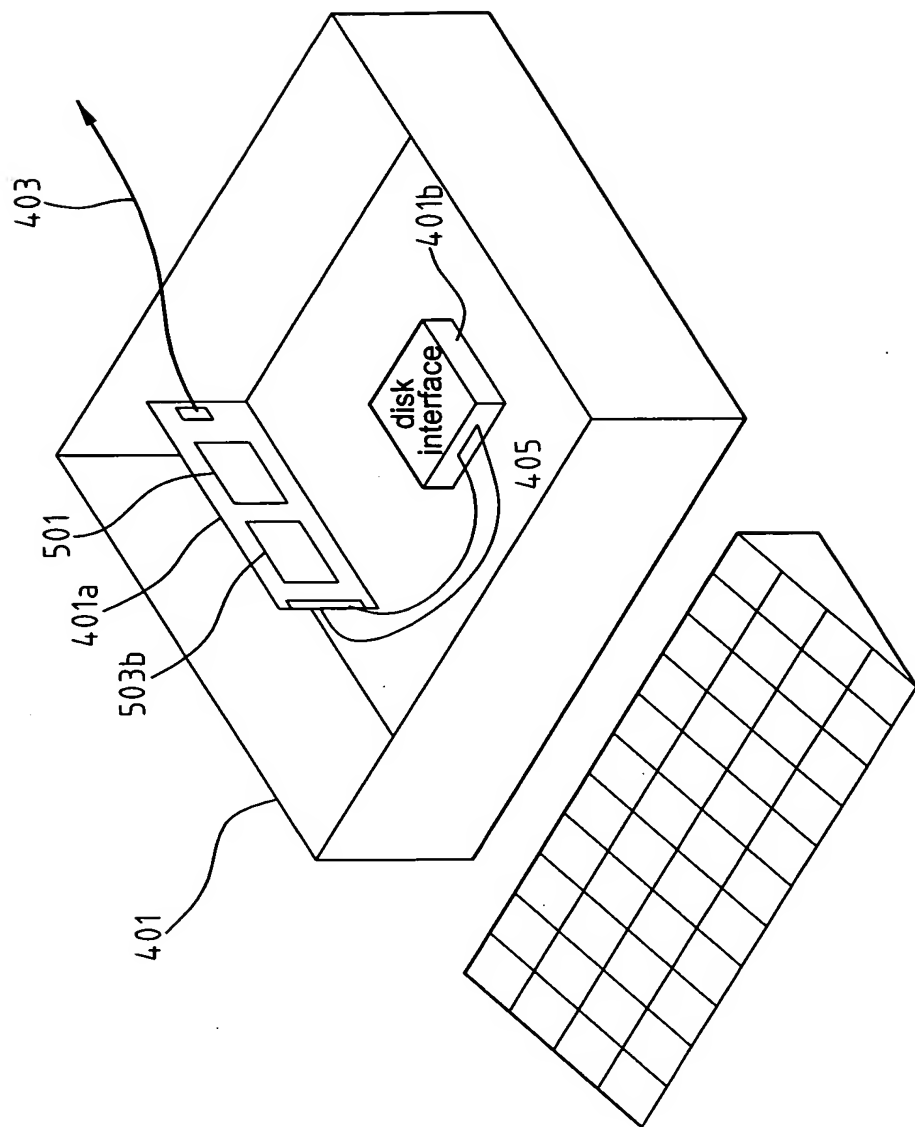
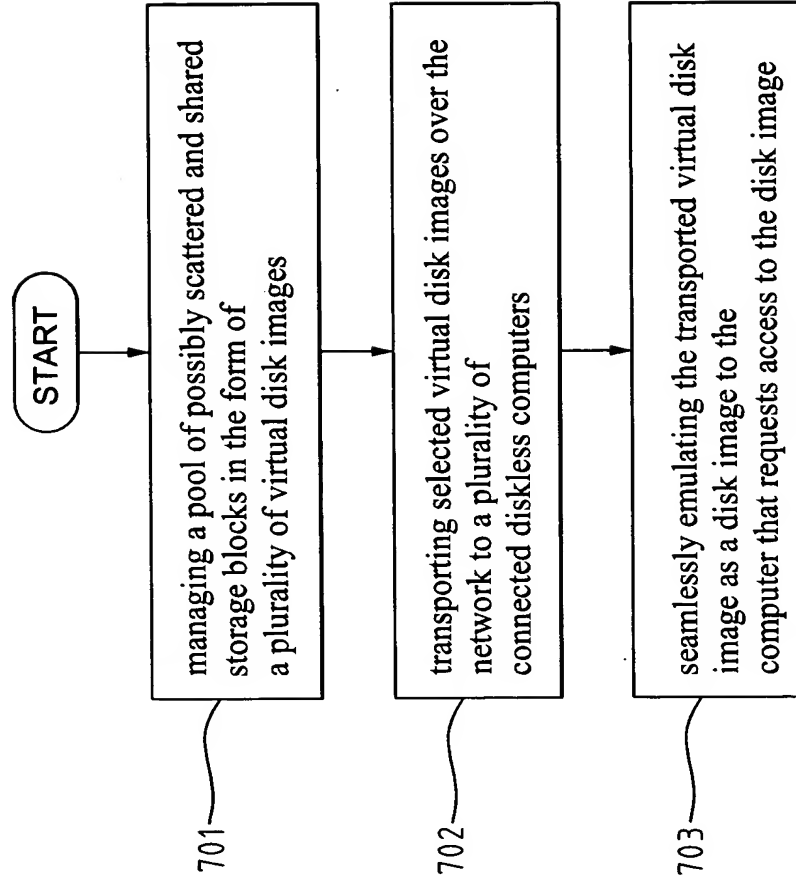


FIG. 5

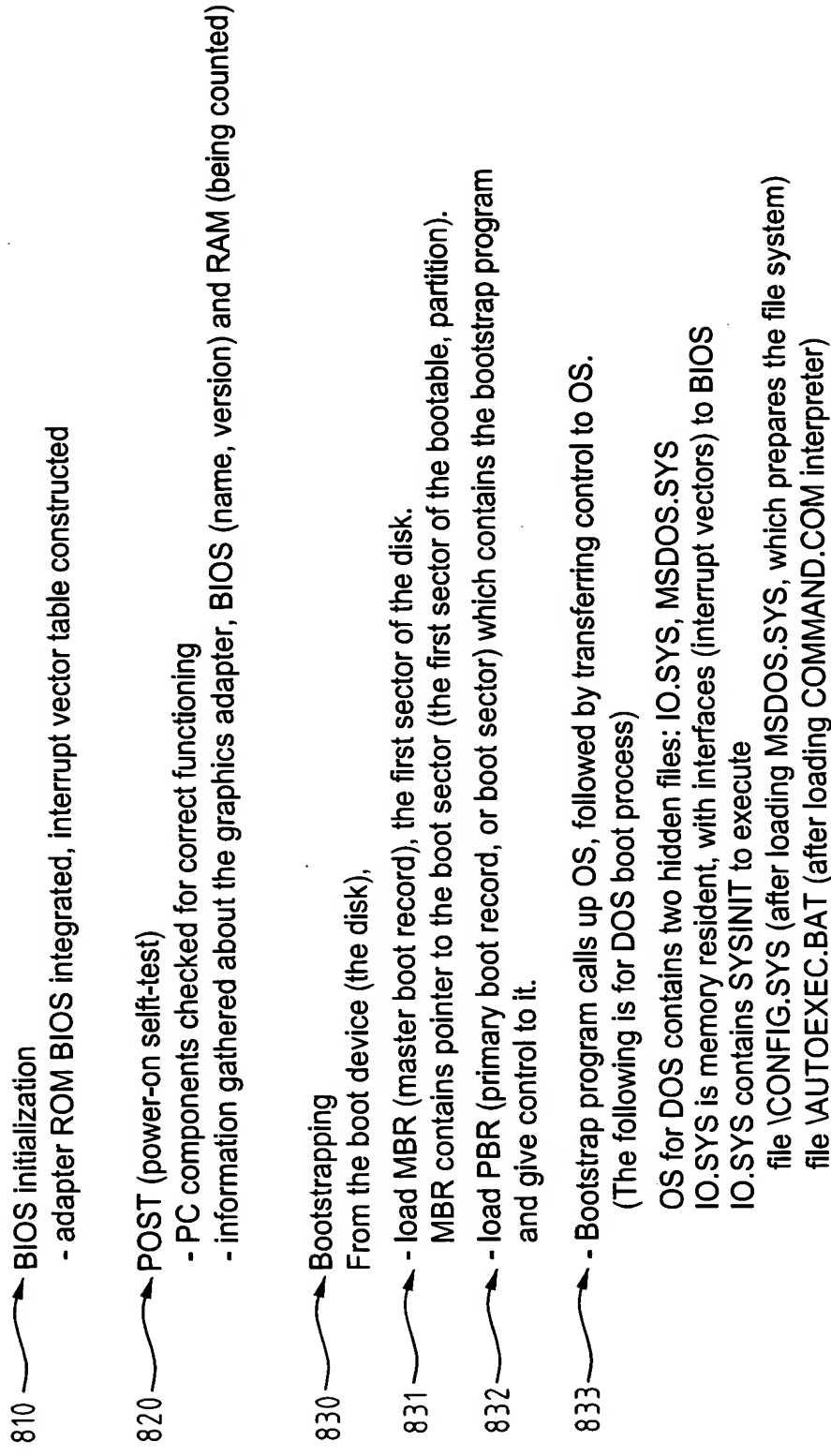


**FIG. 6**



**FIG. 7**

BIOS execution steps contained in ROM chip (activated one by one during the PC startup)



**FIG. 8**